

# RGW80TK65D

## 650V 40A Field Stop Trench IGBT

V <sub>CES</sub>	650V
I <sub>C (100°C)</sub>	23A
V <sub>CE(sat) (Typ.)</sub>	1.5V@I <sub>C</sub> =40A
P <sub>D</sub>	81W

#### Features

- 1) Low Collector Emitter Saturation Voltage
- 2) High Speed Switching
- 3) Low Switching Loss & Soft Switching
- 4) Built in Very Fast & Soft Recovery FRD
- 5) Pb free Lead Plating; RoHS Compliant

### Applications

**PFC** 

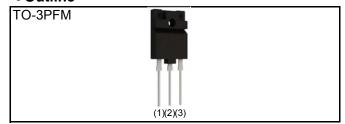
**UPS** 

Welding

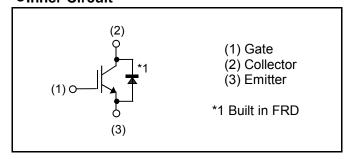
Solar Inverter

ΙH

#### Outline



### ●Inner Circuit



### Packaging Specifications

	Packaging	Tube		
		Reel Size (mm)	ı	
<sub>T</sub> ,	no.	Tape Width (mm)	-	
Туре	/pe	Basic Ordering Unit (pcs)	450	
		Packing Code	C11	
		Marking	RGW80TK65D	

## ● Absolute Maximum Ratings (at T<sub>C</sub> = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V <sub>CES</sub>	650	V
Gate - Emitter Voltage		$V_{GES}$	±30	V
Collector Current	T <sub>C</sub> = 25°C	I <sub>C</sub>	39	Α
Collector Current	T <sub>C</sub> = 100°C	I <sub>C</sub>	23	А
Pulsed Collector Current		I <sub>CP</sub> *1	160	А
Diode Forward Current	T <sub>C</sub> = 25°C	I <sub>F</sub>	27	А
	T <sub>C</sub> = 100°C	I <sub>F</sub>	16	А
Diode Pulsed Forward Current		I <sub>FP</sub> *1	160	А
Power Dissipation	T <sub>C</sub> = 25°C	$P_{D}$	81	W
	T <sub>C</sub> = 100°C	$P_{D}$	40	W
Operating Junction Temperature		T <sub>j</sub>	-40 to +175	°C
Storage Temperature		T <sub>stg</sub>	-55 to +175	°C

<sup>\*1</sup> Pulse width limited by T<sub>imax</sub>.

### ●Thermal Resistance

Parameter	Cumbal	Values			Unit
Farameter	Symbol	Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	ı	1.85	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j-c)}$	-	1	2.79	°C/W

# ●IGBT Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Collector - Emitter Breakdown Voltage	BV <sub>CES</sub>	$I_C = 10 \mu A, V_{GE} = 0 V$	650	-	-	V
Collector Cut - off Current	I <sub>CES</sub>	V <sub>CE</sub> = 650V, V <sub>GE</sub> = 0V	1	1	10	μΑ
Gate - Emitter Leakage Current	I <sub>GES</sub>	$V_{GE} = \pm 30V, V_{CE} = 0V$	-	-	±200	nA
Gate - Emitter Threshold Voltage	$V_{\text{GE(th)}}$	V <sub>CE</sub> = 5V, I <sub>C</sub> = 26.0mA	5.0	6.0	7.0	V
Collector - Emitter Saturation Voltage	V <sub>CE(sat)</sub>	$I_C = 40A, V_{GE} = 15V$ $T_j = 25^{\circ}C$ $T_j = 175^{\circ}C$	-	1.5 1.85	1.9 -	V

## ●IGBT Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Daramatar	Cymphal	Conditions		Unit		
Parameter	Symbol		Min.	Тур.	Max.	Ullit
Input Capacitance	C <sub>ies</sub>	V <sub>CE</sub> = 30V	-	3320	-	
Output Capacitance	C <sub>oes</sub>	V <sub>GE</sub> = 0V	-	83	-	pF
Reverse Transfer Capacitance	C <sub>res</sub>	f = 1MHz	-	60	-	
Total Gate Charge	$Q_g$	V <sub>CE</sub> = 400V	-	110	-	
Gate - Emitter Charge	$Q_{ge}$	I <sub>C</sub> = 40A	-	23	-	nC
Gate - Collector Charge	$Q_{gc}$	V <sub>GE</sub> = 15V	-	41	-	
Turn - on Delay Time	t <sub>d(on)</sub>	I <sub>C</sub> = 40A, V <sub>CC</sub> = 400V	-	44	-	
Rise Time	t <sub>r</sub>	$V_{GE} = 15V, R_G = 10\Omega$	-	17	-	
Turn - off Delay Time	t <sub>d(off)</sub>	T <sub>j</sub> = 25°C	-	143	-	ns
Fall Time	t <sub>f</sub>	Inductive Load	-	34	-	
Turn - on Switching Loss	E <sub>on</sub>	*E <sub>on</sub> includes diode	-	0.76	-	
Turn - off Switching Loss	E <sub>off</sub>	reverse recovery	-	0.72	-	mJ
Turn - on Delay Time	t <sub>d(on)</sub>	I <sub>C</sub> = 40A, V <sub>CC</sub> = 400V	-	41	-	
Rise Time	t <sub>r</sub>	$V_{GE} = 15V, R_{G} = 10\Omega$	-	18	-	
Turn - off Delay Time	t <sub>d(off)</sub>	T <sub>j</sub> = 175°C	-	158	-	ns
Fall Time	t <sub>f</sub>	Inductive Load	-	70	-	
Turn - on Switching Loss	E <sub>on</sub>	*E <sub>on</sub> includes diode	-	0.76	-	
Turn - off Switching Loss	E <sub>off</sub>	reverse recovery	-	0.91	-	mJ
		I <sub>C</sub> = 160A, V <sub>CC</sub> = 520V				
Reverse Bias Safe Operating Area	RBSOA	V <sub>P</sub> = 650V, V <sub>GE</sub> = 15V	FULL SQUARE			-
		$R_G = 100\Omega, T_j = 175^{\circ}C$				

# ●FRD Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
- Farameter			Min.	Тур.	Max.	Offic
Diode Forward Voltage	$V_{F}$	$I_F = 20A$ $T_j = 25$ °C $T_j = 175$ °C	-	1.45 1.55	1.9 -	V
Diode Reverse Recovery Time	t <sub>rr</sub>		-	92	-	ns
Diode Peak Reverse Recovery Current	l <sub>rr</sub>	$I_F = 20A$ $V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ $T_j = 25^{\circ}C$	1	6.7	-	A
Diode Reverse Recovery Charge	$Q_{rr}$		-	0.34	-	μC
Diode Reverse Recovery Energy	E <sub>rr</sub>		-	14.1	-	μJ
Diode Reverse Recovery Time	t <sub>rr</sub>	$I_F = 20A$ $V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ $T_j = 175^{\circ}C$	1	123	-	ns
Diode Peak Reverse Recovery Current	I <sub>rr</sub>		-	7.8	-	А
Diode Reverse Recovery Charge	Q <sub>rr</sub>		1	0.59	-	μC
Diode Reverse Recovery Energy	E <sub>rr</sub>		-	30.7	-	μJ

Fig.1 Power Dissipation vs. Case Temperature

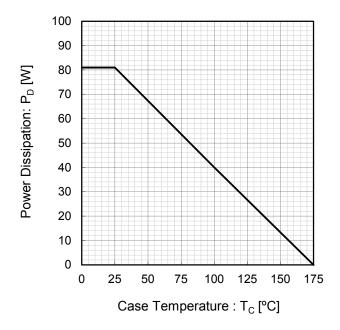


Fig.2 Collector Current vs. Case Temperature

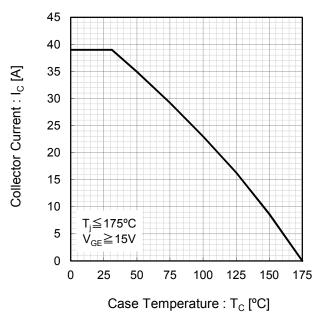


Fig.3 Forward Bias Safe Operating Area

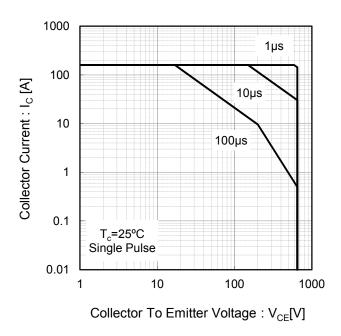


Fig.4 Reverse Bias Safe Operating Area

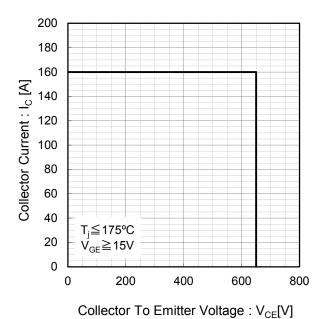


Fig.5 Typical Output Characteristics

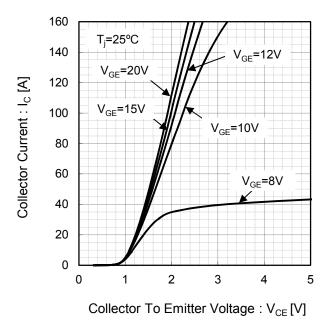
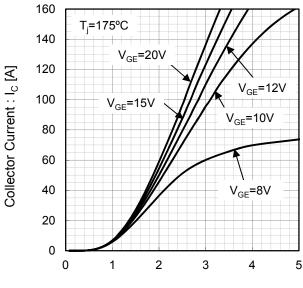


Fig.6 Typical Output Characteristics



Collector To Emitter Voltage : V<sub>CE</sub> [V]

Fig.7 Typical Transfer Characteristics

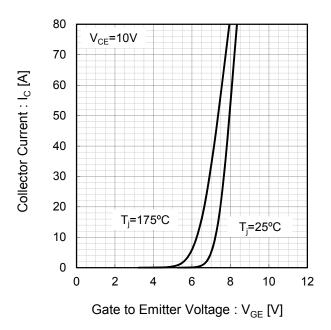


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature

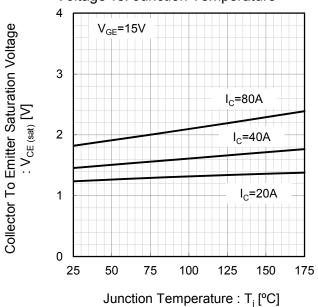
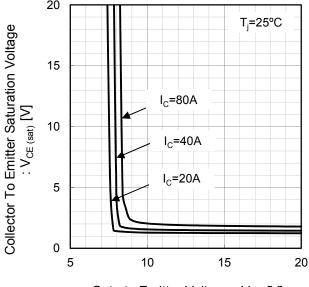
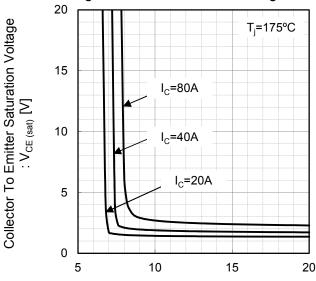


Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



Gate to Emitter Voltage :  $V_{GE}[V]$ 

Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



Gate to Emitter Voltage : V<sub>GE</sub> [V]

Fig.11 Typical Switching Time vs. Collector Current

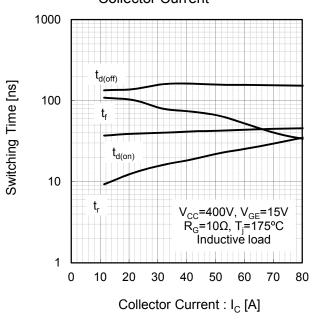


Fig.12 Typical Switching Time vs. Gate Resistance

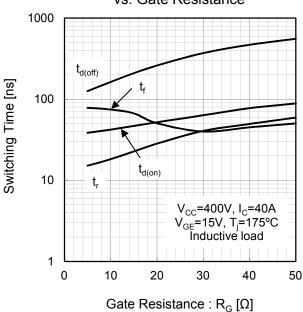


Fig.13 Typical Switching Energy Losses vs.
Collector Current

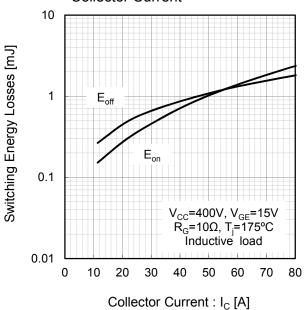


Fig.14 Typical Switching Energy Losses vs. Gate Resistance

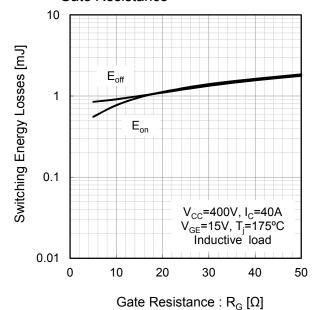
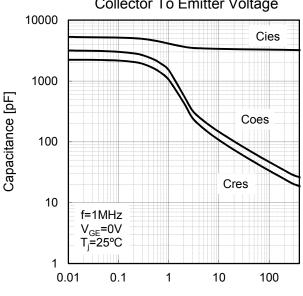


Fig.15 Typical Capacitance vs.
Collector To Emitter Voltage



Collector To Emitter Voltage :  $V_{CE}[V]$ 

Fig.16 Typical Gate Charge

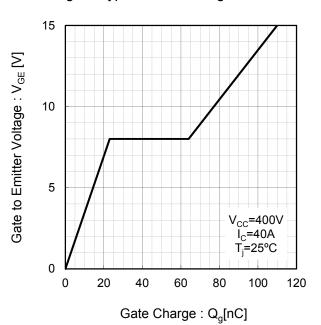


Fig.17 Typical Diode Forward Current vs. Forward Voltage 160 140 Forward Current : I<sub>F</sub> [A] 120 100 80 T<sub>i</sub>=25°C 60 40 T<sub>i</sub>=175°C 20 0 2 0 3 5 Forward Voltage: V<sub>F</sub>[V]

vs. Forward Current 400 Reverse Recovery Time  $: t_{\rm rr} \, [\text{ns}]$ 300 200 T<sub>i</sub>=175°C 100  $V_{CC}$ =400V di<sub>F</sub>/dt=200A/µs T<sub>i</sub>=25°C Inductive load 0 0 10 20 30 40 50 60 70 80 Forward Current : I<sub>F</sub> [A]

Fig.18 Typical Diode Reverse Recovery Time

Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current 20 Reverse Recovery Curren : I<sub>rr</sub>[A] 15 10 T<sub>i</sub>=175°C 5 V<sub>CC</sub>=400V di<sub>F</sub>/dt=200A/μs T<sub>i</sub>=25°C Inductive load 10 20 30 40 50 70 80 0 60 Forward Current: I<sub>F</sub> [A]

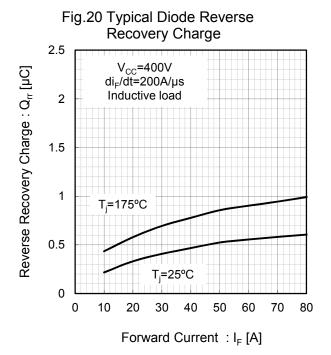


Fig.21 Typical IGBT Transient Thermal Impedance

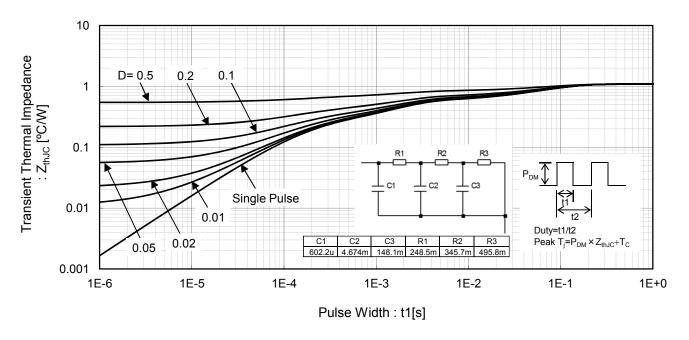
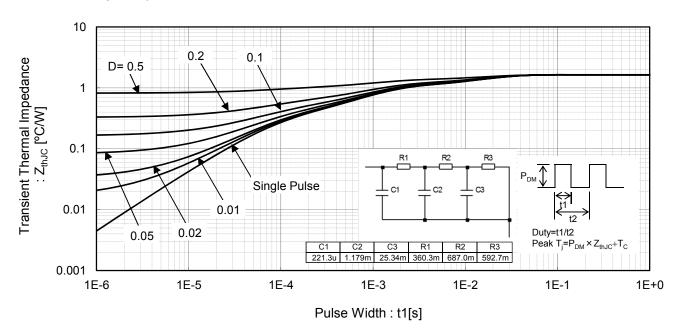


Fig.22 Typical Diode Transient Thermal Impedance



### •Inductive Load Switching Circuit and Waveform

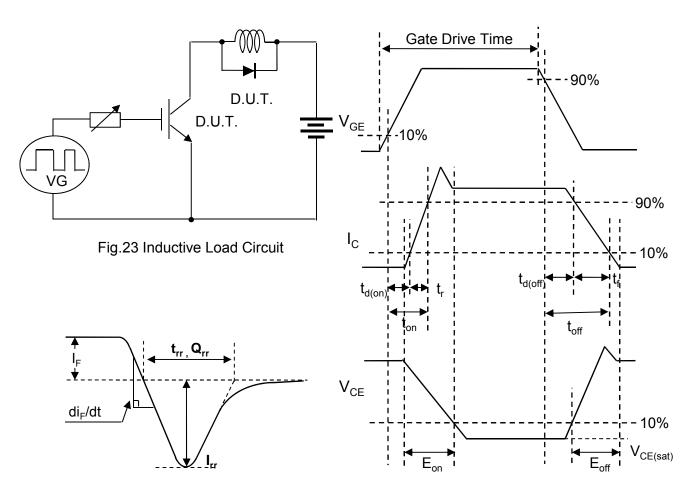


Fig.25 Diode Reverce Recovery Waveform

Fig.24 Inductive Load Waveform

#### Notes

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