



# SCH1434

## N-Channel Power MOSFET 30V, 2A, 165mΩ, Single SCH6

**ON Semiconductor®**
<http://onsemi.com>

### Features

- 1.8V drive
- Halogen free compliance
- Protection diode in

### Specifications

#### Absolute Maximum Ratings at Ta=25°C

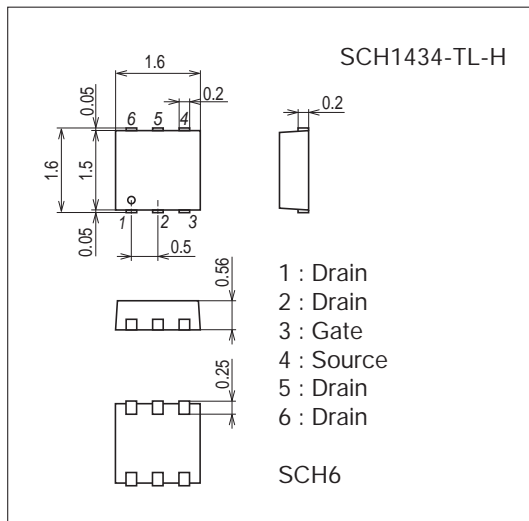
Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		30	V
Gate-to-Source Voltage	V <sub>GSS</sub>		±12	V
Drain Current (DC)	I <sub>D</sub>		2	A
Drain Current (Pulse)	I <sub>DP</sub>	PW≤10μs, duty cycle≤1%	8	A
Allowable Power Dissipation	P <sub>D</sub>	When mounted on ceramic substrate (900mm <sup>2</sup> ×0.8mm)	0.8	W
Channel Temperature	T <sub>ch</sub>		150	°C
Storage Temperature	T <sub>stg</sub>		-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

### Package Dimensions

unit : mm (typ)

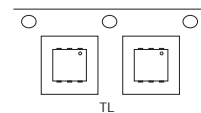
7028-002



### Product & Package Information

- Package : SCH6
- JEITA, JEDEC : SOT-563
- Minimum Packing Quantity : 5,000 pcs./reel

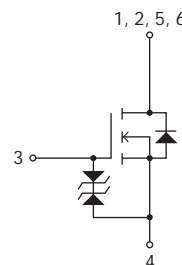
### Packing Type : TL



### Marking



### Electrical Connection

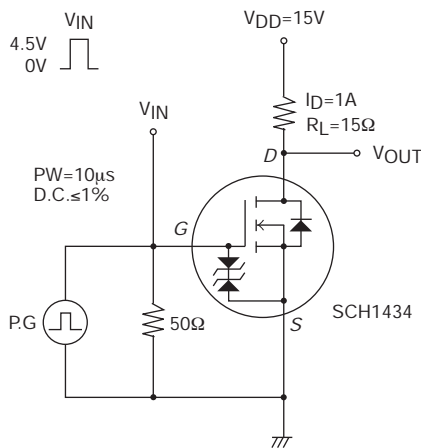


# SCH1434

## Electrical Characteristics at Ta=25°C

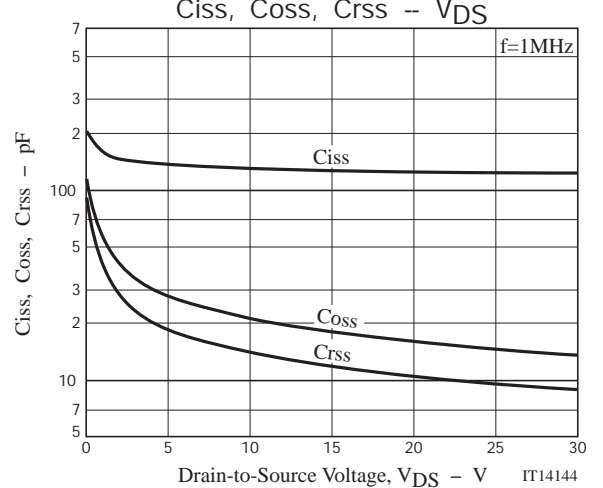
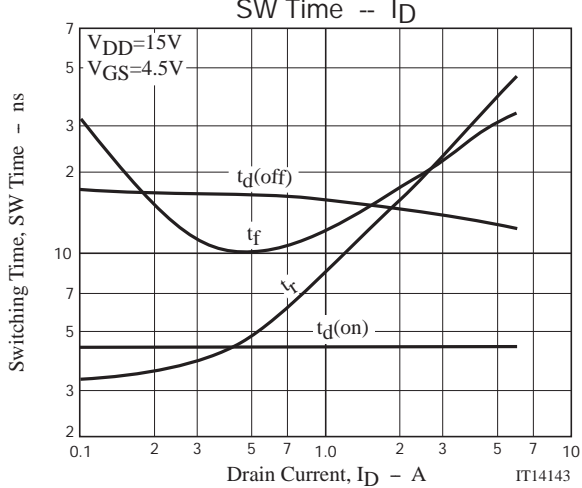
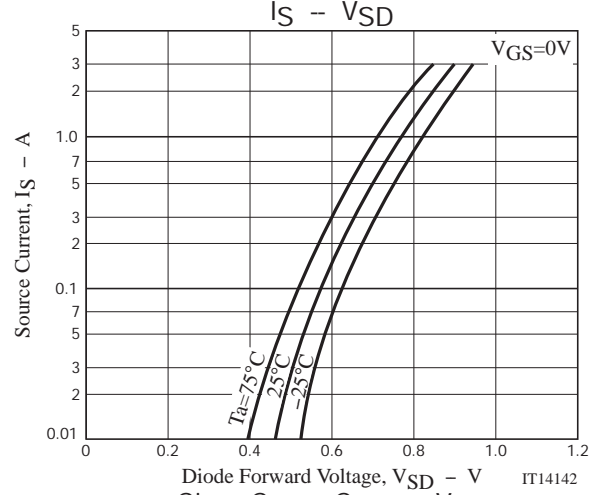
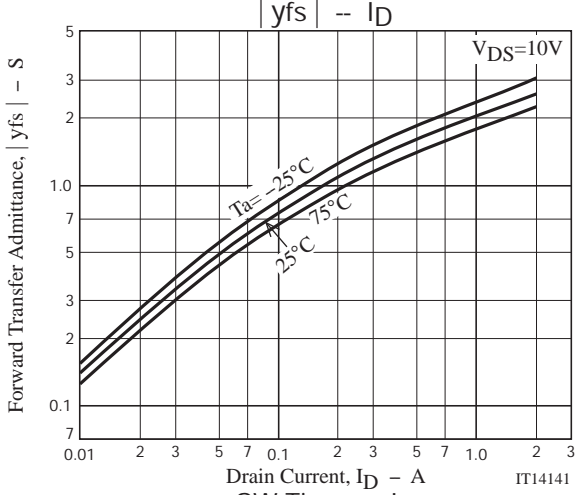
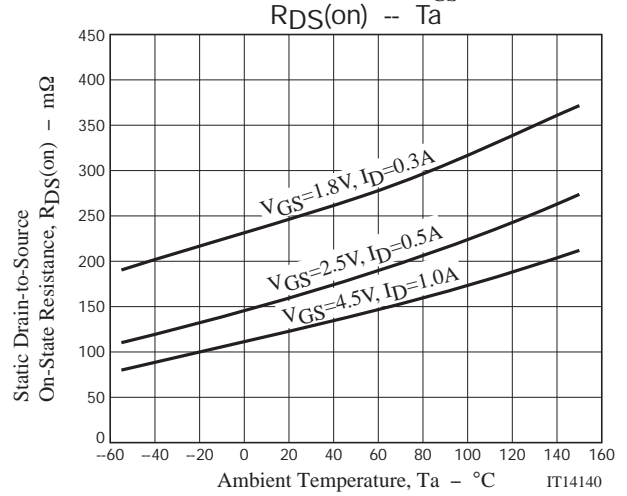
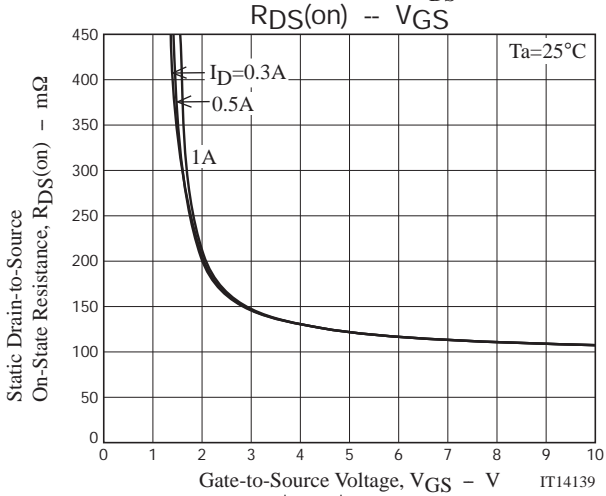
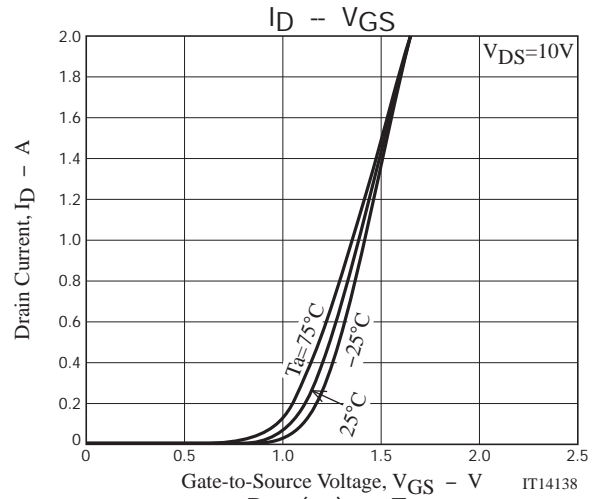
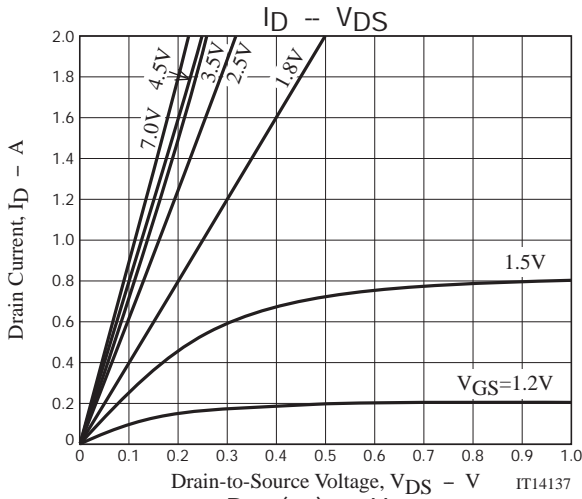
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> =1mA, V <sub>GS</sub> =0V	30			V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V			1	μA
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0V			±10	μA
Cutoff Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	0.4		1.3	V
Forward Transfer Admittance	y <sub>fs</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =1A		2.0		S
Static Drain-to-Source On-State Resistance	R <sub>DS(on)1</sub>	I <sub>D</sub> =1A, V <sub>GS</sub> =4.5V		125	165	mΩ
	R <sub>DS(on)2</sub>	I <sub>D</sub> =0.5A, V <sub>GS</sub> =2.5V		165	235	mΩ
	R <sub>DS(on)3</sub>	I <sub>D</sub> =0.3A, V <sub>GS</sub> =1.8V		250	375	mΩ
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =10V, f=1MHz		130		pF
Output Capacitance	C <sub>oss</sub>			21		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			14		pF
Turn-ON Delay Time	t <sub>d(on)</sub>		See specified Test Circuit.		4.4	
Rise Time	t <sub>r</sub>			8.7		ns
Turn-OFF Delay Time	t <sub>d(off)</sub>			16		ns
Fall Time	t <sub>f</sub>			12		ns
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =10V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =2A			1.7	
Gate-to-Source Charge	Q <sub>gs</sub>			0.25		nC
Gate-to-Drain "Miller" Charge	Q <sub>gd</sub>			0.38		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =2A, V <sub>GS</sub> =0V		0.85	1.2	V

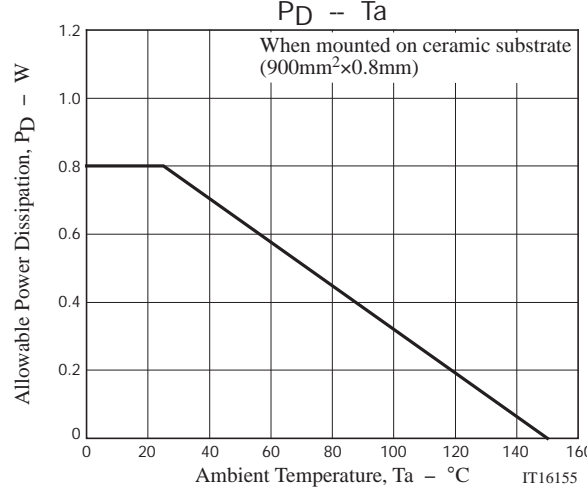
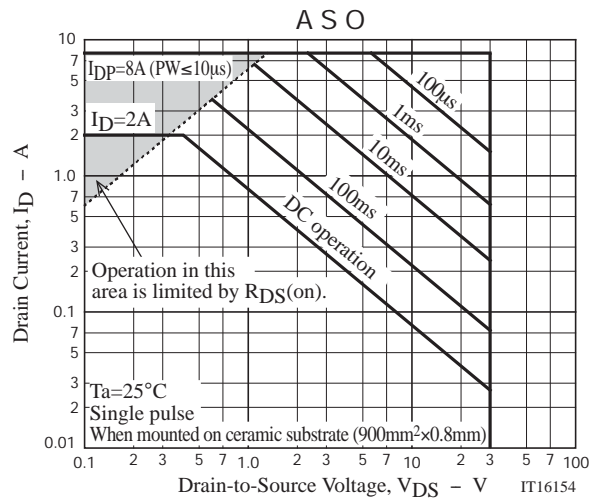
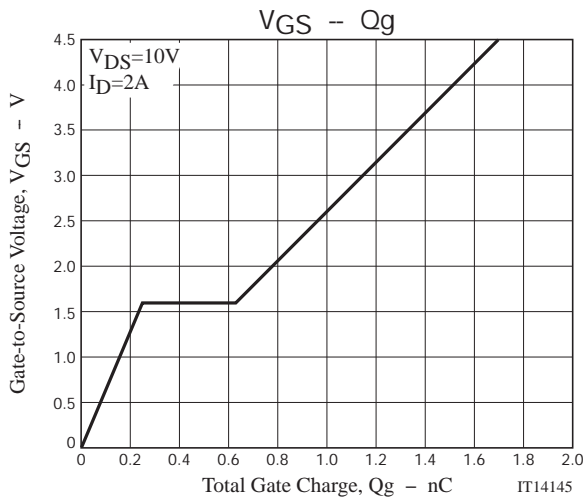
## Switching Time Test Circuit



## Ordering Information

Device	Package	Shipping	memo
SCH1434-TL-H	SCH6	5,000pcs./reel	Pb Free and Halogen Free





Taping Specification

SCH1434-TL-H

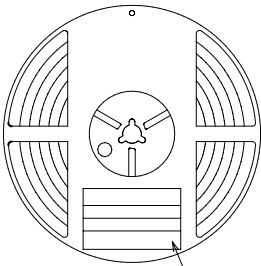
1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
SCH6	SCH6	5,000	25,000	150,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

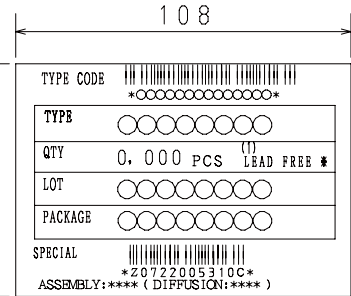
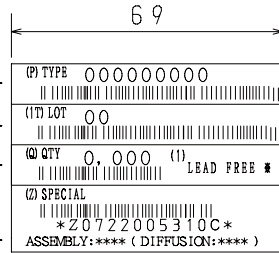
Packing method

Reel label, Inner box label  
(unit: mm)

Outer box label  
It is a label at the time of factory shipments.  
The form of a label may change in physical distribution process.



Type No. →  
LOT No. →  
Quantity →  
Origin →



Reel label

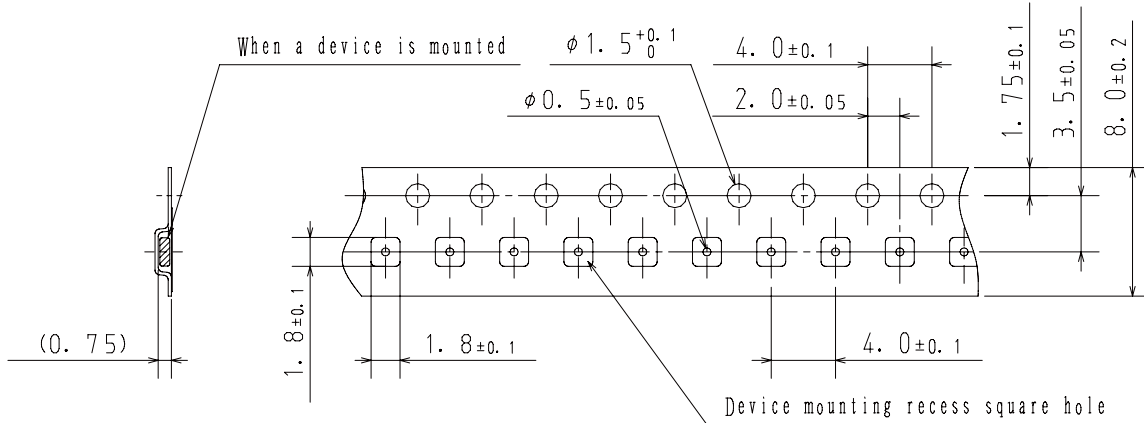
NOTE (1)

The LEAD FREE \* description shows that the surface treatment of the terminal is lead free.

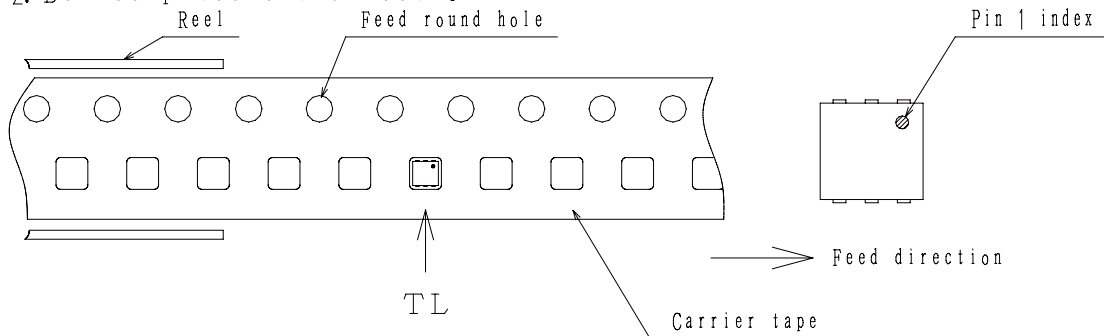
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



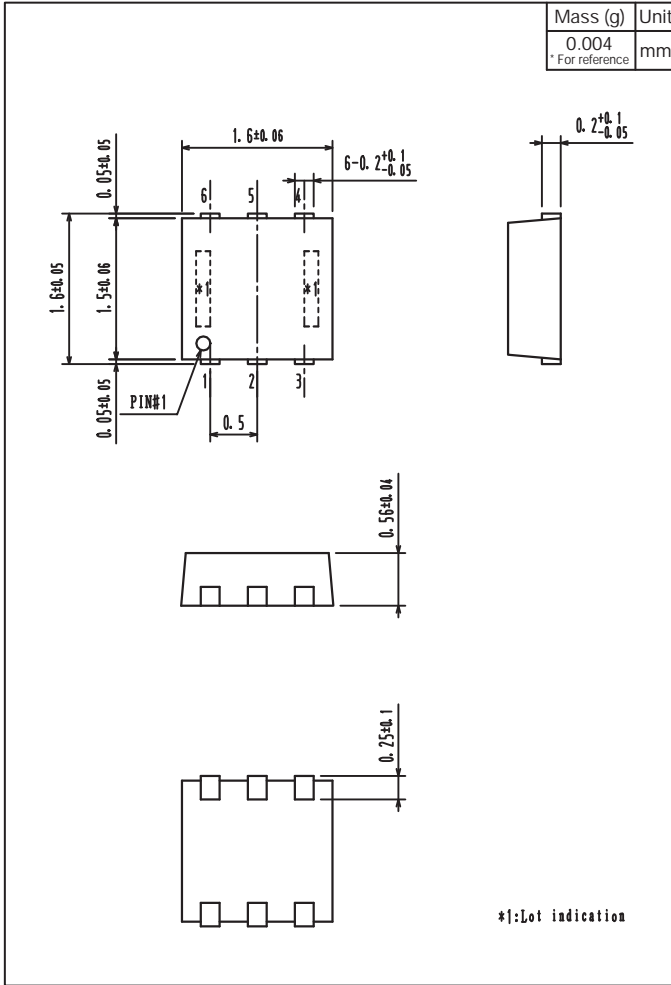
2-2. Device placement direction



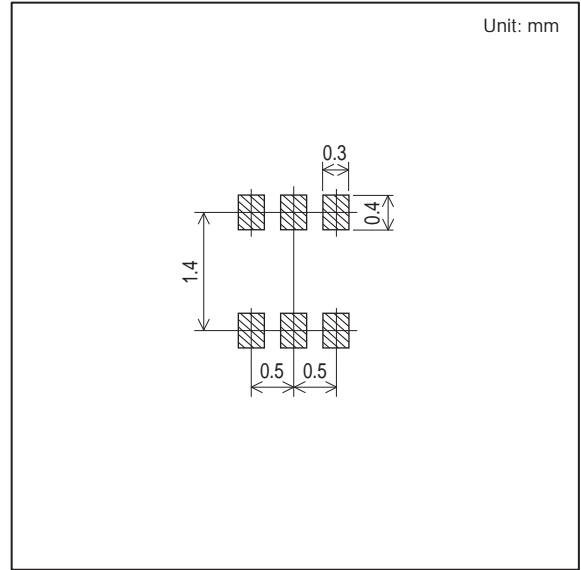
Those with pin 1 index on the feed hole side.....TL

# SCH1434

## Outline Drawing SCH1434-TL-H



## Land Pattern Example



Note on usage : Since the SCH1434 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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