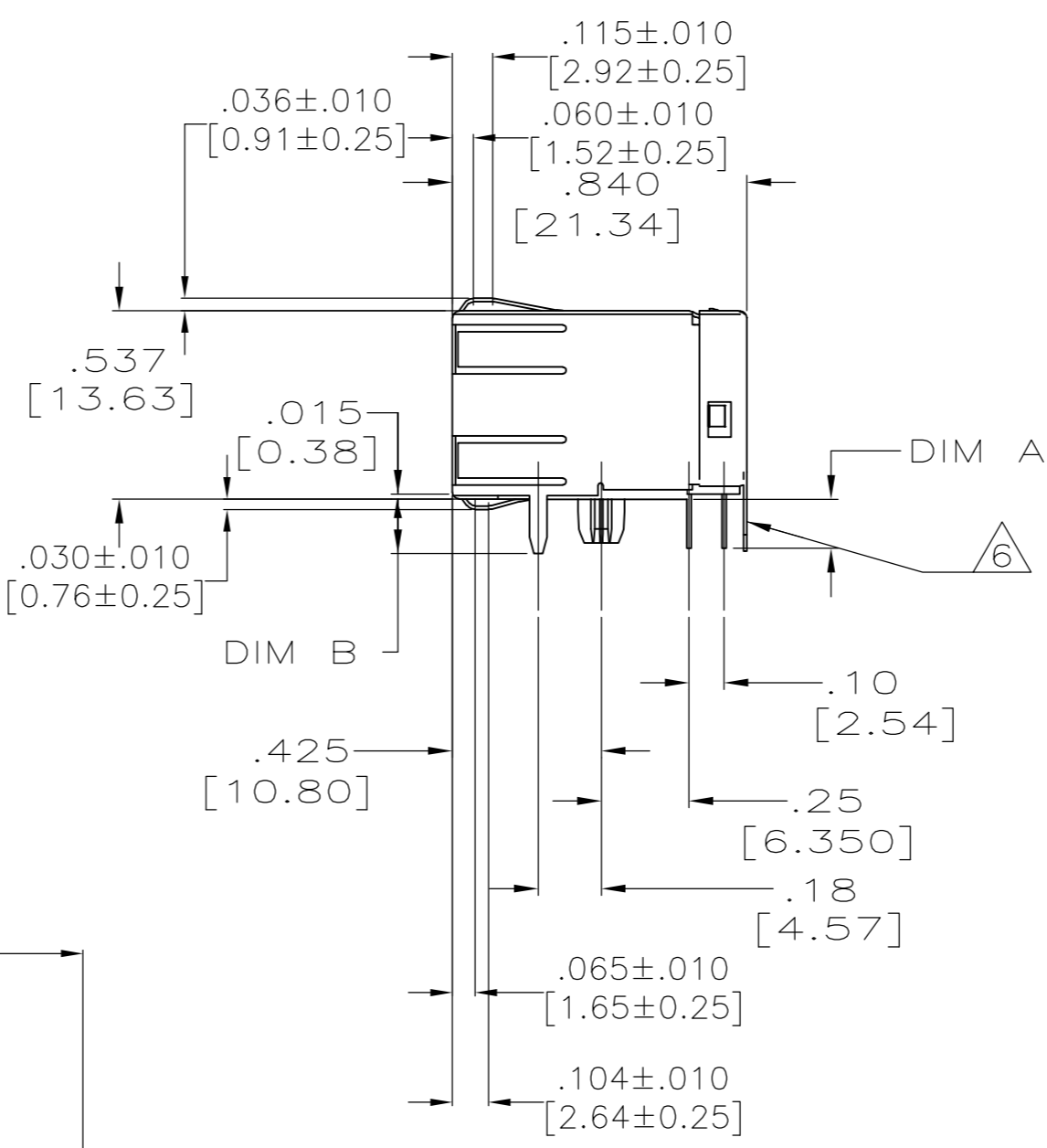
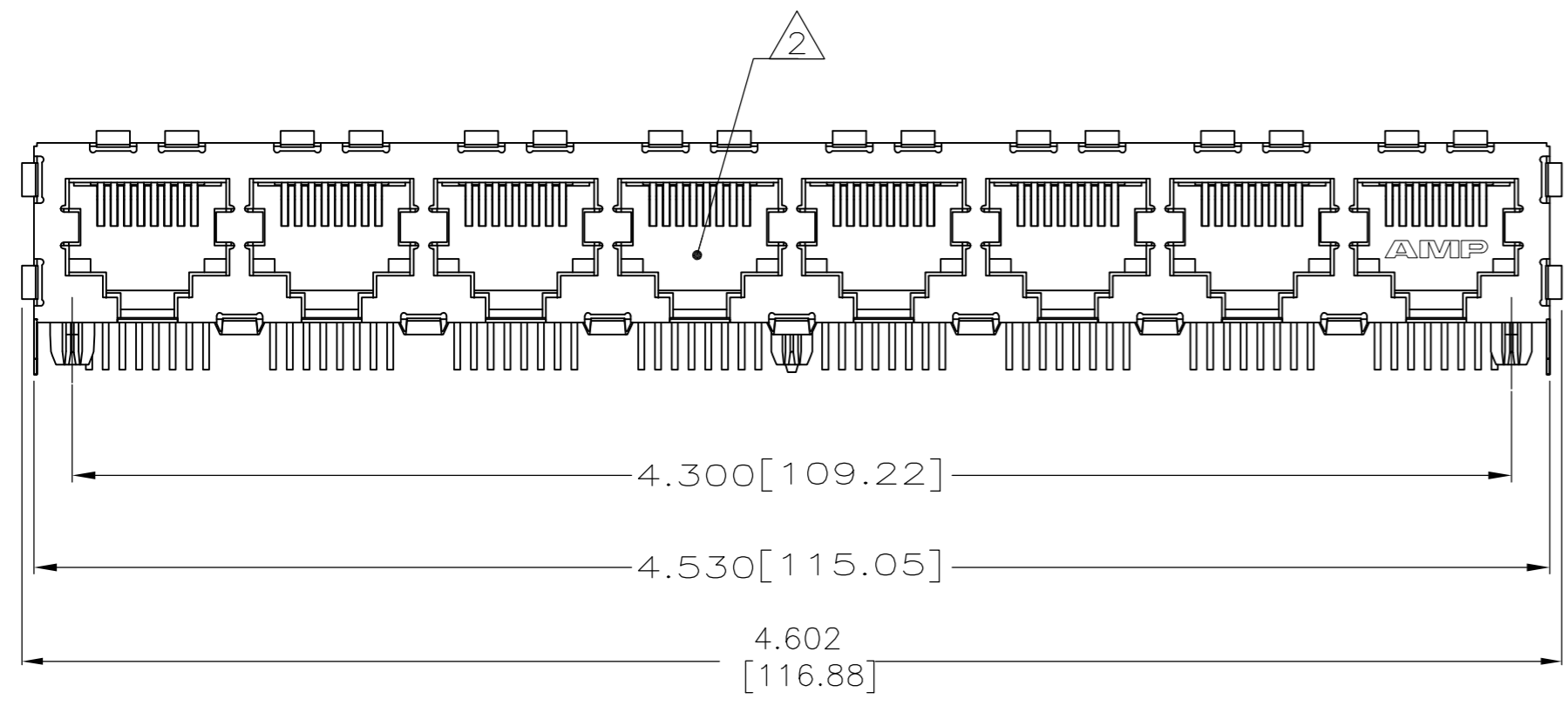


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LOC	DIST	REVISIONS					
		P	LTR	DESCRIPTION	DATE	DWN	APVD
AA	00	F6		ECR-10-016042	03AUG2010	KS	M.W



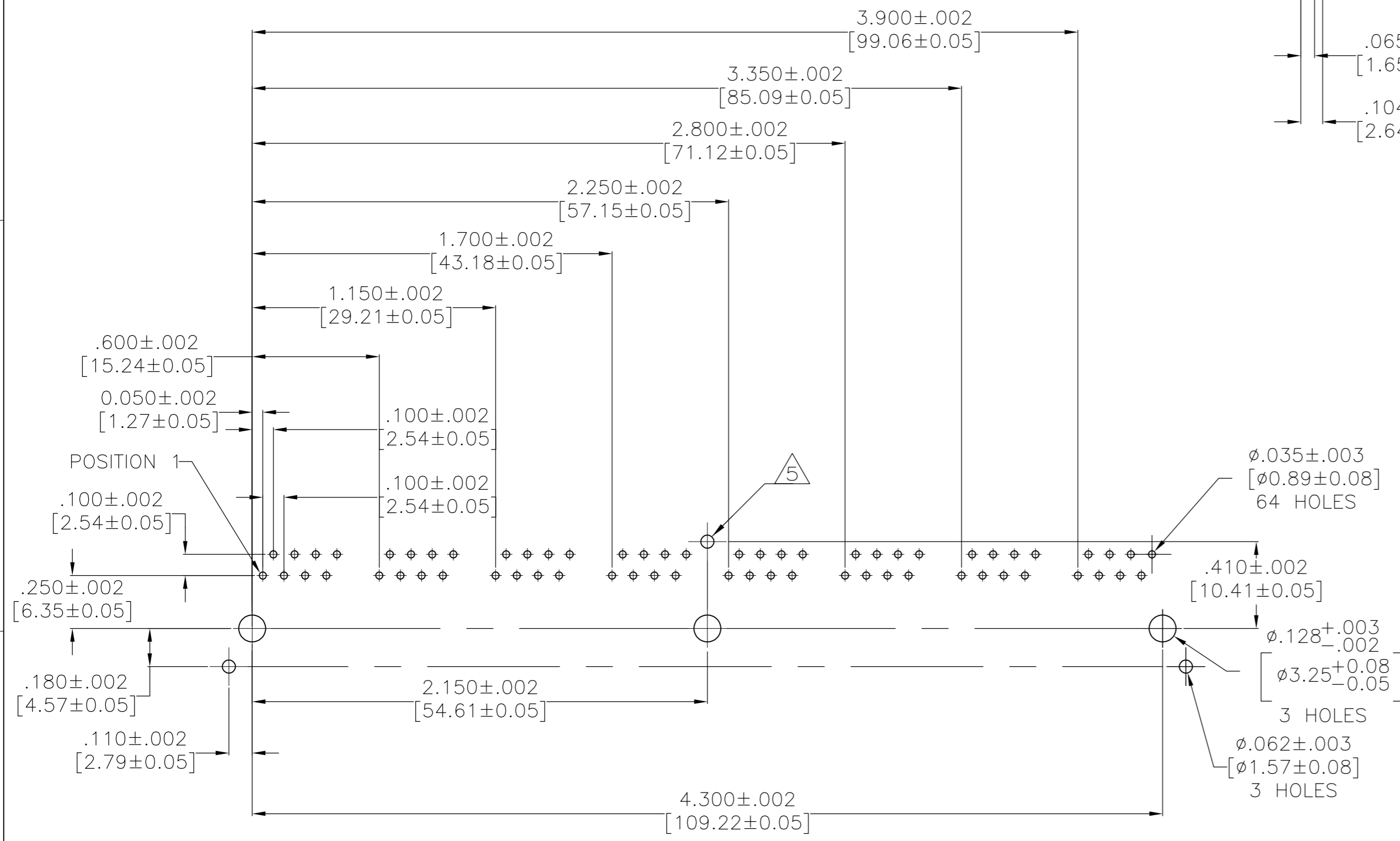
△ MATERIAL:
 HOUSING - NYLON, COLOR: BLACK, UL94V-0.
 OVERMOLDED TERMINAL ARRAY - HTN, BROWN.
 TERMINALS-.013 [0.33] THICK PHOS BRONZE PLATED WITH .000050 [1.27µm] THICK HARD GOLD IN LOCALIZED AREA AND .000150 [3.81 µm] MIN THICK MATTE TIN IN SOLDER AREA OVER .000050 [1.27 µm] MIN THICK NICKEL UNDERPLATE
 SHIELDS - .010 [0.254] THICK COPPER ALLOY PLATED WITH 1.27 µm [0.00050] MINIMUM NICKEL AND 2.03 µm [0.00080] MINIMUM HOT TIN DIP ON PCB GROUND TABS.

△ JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS, PART 68 SUBPART F.
 3. ALL DIMENSIONS SHOWN ARE NOMINAL UNLESS OTHERWISE SPECIFIED.
 4. ALL DIMENSIONS SHOWN IN BRACKETS [] ARE IN MILLIMETERS.

△ THIS HOLE IS NEEDED FOR -1 AND -3 ONLY.
 △ THIS TAB REMOVED ON -2.
 △ THE PART IS PRELIMINARY

△ MATERIAL:
 HOUSING - NYLON, COLOR: BLACK, UL94V-0.
 OVERMOLDED TERMINAL ARRAY - PBT, BROWN.
 TERMINALS-.013 [0.33] THICK PHOS BRONZE PLATED WITH .000050 [1.27µm] THICK HARD GOLD IN LOCALIZED AREA AND .000150 [3.81 µm] MIN THICK MATTE TIN IN SOLDER AREA OVER .000050 [1.27 µm] MIN THICK NICKEL UNDERPLATE
 SHIELDS - .010 [0.254] THICK COPPER ALLOY PLATED WITH 1.27 µm [0.00050] MINIMUM NICKEL AND 2.03 µm [0.00080] MINIMUM HOT TIN DIP ON PCB GROUND TABS.

△ OBSOLETE PARTS: OBSOLETE CIS STREAMLINING PER D.RENAUD/D.SINISI



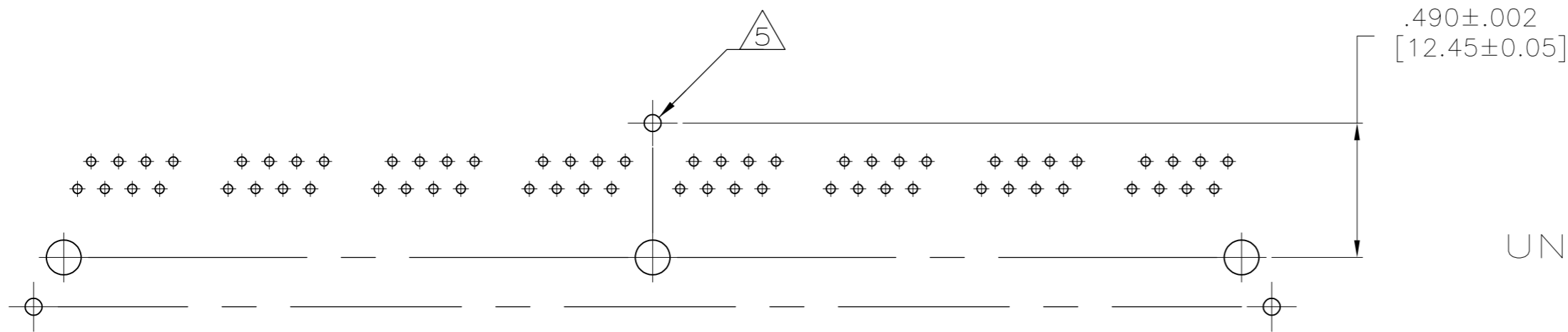
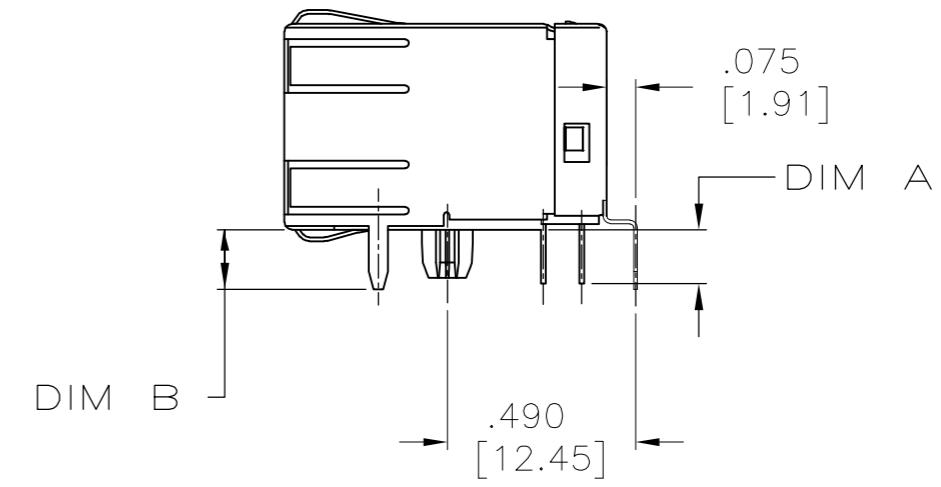
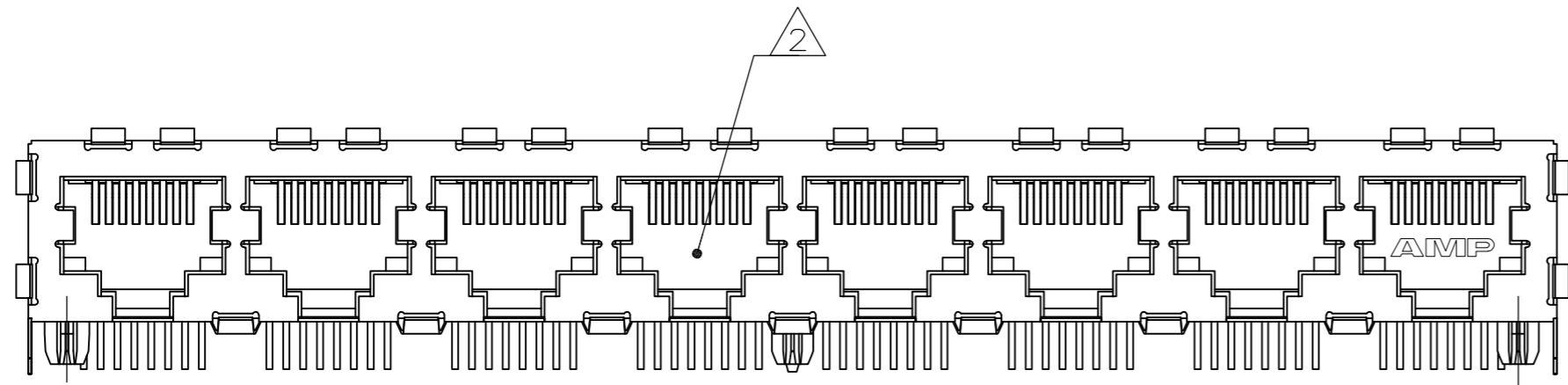
FOR P/N:5406491-1&-2
 SUGGESTED PC BOARD LAYOUT
 COMPONENT SIDE
 SEE APPLICATION SPECIFICATION

CALLOUT	DIM B (INCHES)	DIM A (INCHES)	DESCRIPTION	PART NO.
△	.128±.010	.128±.010	WITH REAR PC BOARD GROUND TAB	5406491-5
△	.128±.010	.128±.010	WITH REAR PC BOARD GROUND TAB	5406491-4 △
△	.155	.140±.015	WITHOUT REAR PC BOARD GROUND TAB	5406491-2 △
△	.155	.140±.015	WITH REAR PC BOARD GROUND TAB	5406491-1

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN Francis Lee 14APRIL2009	Tyco Electronics Corporation Harrisburg, Pa 17105-3608	
DIMENSIONS: INCHES		CHK Denny Zhu 14APRIL2009	NAME	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD Steven Yao 14APRIL2009	PRODUCT SPEC	
0 PLC ± -	1 PLC ± -	108-1163-2		
2 PLC ± .01[0.254]	3 PLC ± .005[0.127]	APPLICATION SPEC		
4 PLC ± -	ANGLES ± -	114-2048		
FINISH		WEIGHT -		
SEE NOTE 1		SCALE A2 00779 C=5406491		
SEE NOTE 1		CUSTOMER DRAWING		

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LOC	DIST	REVISIONS			
P	LTR	DESCRIPTION	DATE	DWN	APVD
AA	00	SEE SHEET 1	-	-	-



FOR P/N:5406491-4;-5
 SUGGESTED PC BOARD LAYOUT
 COMPONENT SIDE
 SEE APPLICATION SPECIFICATION

UNMARKED DIMENSIONS SAME AS P/N:5406491-1

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN Francis Lee 14APRIL2009	Tyco Electronics Corporation Harrisburg, Pa 17105-3608	
DIMENSIONS: INCHES		CHK Denny Zhu 14APRIL2009	NAME	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD Steven Yao 14APRIL2009	MODULAR JACK ASSEMBLY, PANEL GROUND, 8 PORT, 8 POSITION, RIGHT ANGLE, SHIELDED, CATEGORY 5	
0 PLC ± -		PRODUCT SPEC	SIZE	CAGE CODE
1 PLC ± -		108-1163-2	A2	00779
2 PLC ± .01[0.254]		APPLICATION SPEC	DRAWING NO	
3 PLC ± .005[0.127]		114-2048	C=5406491	
4 PLC ± -		WEIGHT	RESTRICTED TO	
ANGLES ± -		-	-	
MATERIAL SEE NOTE 1	FINISH SEE NOTE 1	CUSTOMER DRAWING	SCALE 2:1	SHEET 2 OF 2
			REV F6	