

ZXMP3F37N8 30V SO8 P-channel enhancement mode MOSFET

Summary

V _{(BR)DSS} (V)	R _{DS(on)} (Ω)	I _D (A)
-30	0.025 @ V _{GS} =-10V	-10.7
	0.041 @ V _{GS} =-4.5V	



Description

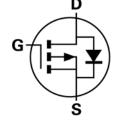
This new generation Trench MOSFET from Zetex has been designed to minimize the on-state resistance (R_{DS(on)}) and yet maintain superior switching performance making it ideal for high efficiency power management applications.

Features

- · Low on-resistance
- · Fast switching speed
- Low gate drive
- SO8 package

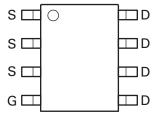
Applications

- DC-DC Converters
- · Power management functions
- · Disconnect switches
- Motor control



Ordering information

Device	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXMP3F37N8TA	7	12	500



Device marking

ZXMP 3F37

ZXMP3F37N8

Absolute maximum ratings

Parameter	Symbol	Limit	Unit
Drain-Source voltage	V_{DSS}	-30	V
Gate-Source voltage	V_{GS}	±20	V
Continuous Drain current @ V _{GS} = -10V; T _A =25°C (b)	I _D	-8.5	V
@ V_{GS} = -10V; T_A =70°C		-6.8	
@ V _{GS} = -10V; T _A =25°C ^(a)		-6.4	
@ V _{GS} = -10V; T _L =25°C ^(d)		-10.7	
Pulsed Drain current (c)	I _{DM}	-39.5	А
Continuous Source current (Body diode) (b)	I _S	-4.4	А
Pulsed Source current (Body diode) (c)	I _{SM}	-39.5	А
Power dissipation at T _A =25°C ^(a) Linear derating factor	P _D	1.56 12.5	W mW/°C
Power dissipation at T _A =25°C ^(b) Linear derating factor	P _D	2.8 22.2	W mW/°C
Power dissipation at T _L =25°C ^(d) Linear derating factor	PD	4.4 35.4	W mW/°C
Operating and storage temperature range	T _j , T _{stg}	-55 to 150	°C

Thermal resistance

Parameter	Symbol	Value	Unit
Junction to ambient ^(a)	$R_{ heta JA}$	80	°C/W
Junction to ambient ^(b)	$R_{ heta JA}$	45	°C/W
Junction to lead ^(d)	$R_{ heta JL}$	28.26	°C/W

NOTES:

⁽a) For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still

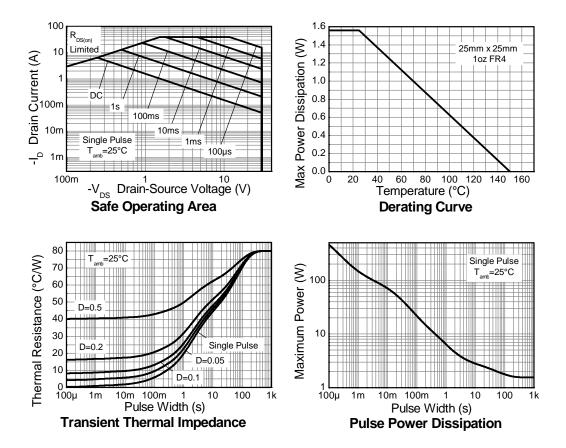
⁽b) Mounted on FR4 PCB measured at $t \le 10$ sec.

⁽c) Repetitive rating on 25mm x 25mm FR4 PCB, D=0.02, pulse width 300us – pulse width limited by maximum junction temperature.

⁽d) Thermal resistance from junction to solder-point (at the end of the drain lead).

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Thermal characteristics



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Electrical characteristics (at T_{amb} = 25°C unless otherwise stated)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Static						
Drain-Source breakdown voltage	V _{(BR)DSS}	-30			V	$I_D = -250 \mu A, V_{GS} = 0 V$
Zero Gate voltage Drain current	I _{DSS}			-1.0	μA	V _{DS} =-30V, V _{GS} =0V
Gate-Body leakage	I _{GSS}			100	nA	$V_{GS}=\pm20V, V_{DS}=0V$
Gate-Source threshold voltage	V _{GS(th)}	-1.3		-2.5	V	$I_D=$ -250 μ A, $V_{DS}=V_{GS}$
Static Drain-Source on-state resistance (*)	R _{DS(on)}			0.025 0.041	Ω	V _{GS} = -10V, I _D = -7.1A V _{GS} = -4.5V, I _D = -5.5A
Forward Transconductance (*) (†)	9fs		18.6		S	V _{DS} = -15V, I _D = -7.1A
Dynamic ^(†)						
Input capacitance	C _{iss}		1678		pF	
Output capacitance	C _{oss}		303		pF	V _{DS} = -15V, V _{GS} =0V
Reverse transfer capacitance	C _{rss}		178		pF	f=1MHz
Switching (‡) (†)						
Turn-on-delay time	t _{d(on)}		3.5		ns	
Rise time	t _r		4.9		ns	V _{DD} = -15V, V _{GS} = -10V
Turn-off delay time	t _{d(off)}		44		ns	I _D = -1A
Fall time	t _f		28		ns	$R_G \cong 6.0\Omega$,
Gate charge						
Total Gate charge	Qg		31.6		nC	
Gate-Source charge	Q _{gs}		4.3		nC	$V_{DS} = -15V, V_{GS} = -10V$
Gate-Drain charge	Q _{gd}		6.2		nC	I _D = -7.1A
Source-Drain diode		_				
Diode forward voltage (*)	V _{SD}		-0.80	-1.2	V	I _S = -1.7A,V _{GS} =0V
Reverse recovery time (‡)	t _{rr}		16.2		ns	I _S = -2.2A,di/dt=100A/μs
Reverse recovery charge ^(‡)	Q _{rr}		10		nC	7152.2Λ,αι/αι=100Α/μS

NOTES:

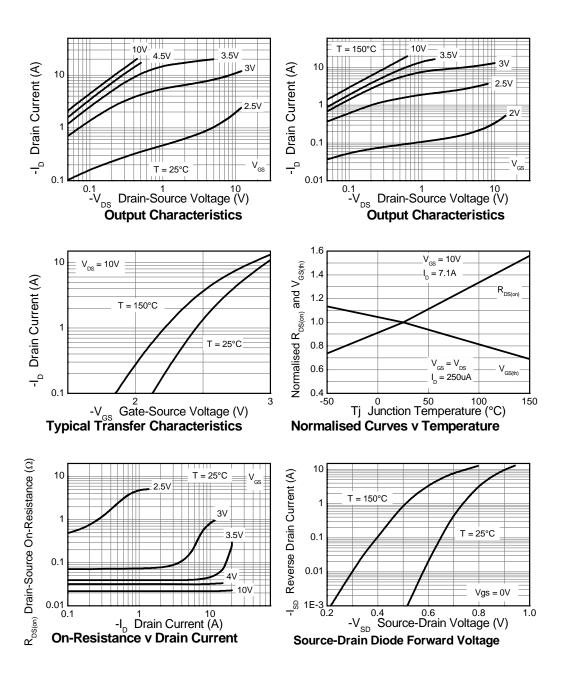
^(*) Measured under pulsed conditions. Pulse width $\leq 300 \mu s;$ duty cycle $\leq 2 \%.$

^(†)Switching characteristics are independent of operating junction temperature.

^(‡)For design aid only, not subject to production testing

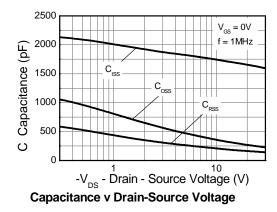
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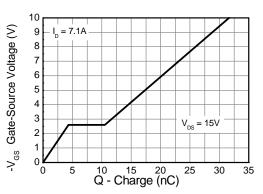
Typical characteristics



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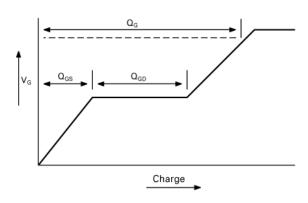
Typical characteristics

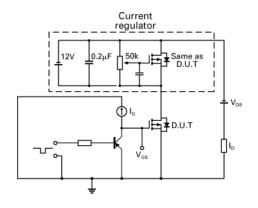




Gate-Source Voltage v Gate Charge

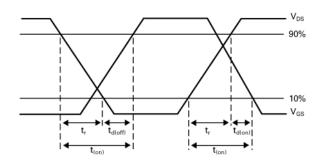
Test circuits

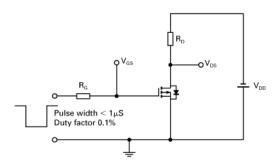




Basic gate charge waveform

Gate charge test circuit



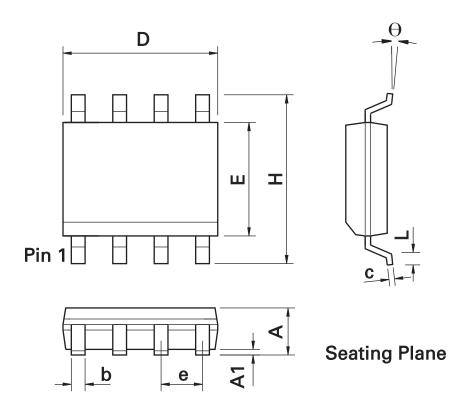


Switching time waveforms

Switching time test circuit

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Package outline SO8



SO8 Package Information

DIM	Inc	hes	Millim	neters	DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
Α	0.053	0.069	1.35	1.75	е	0.050	BSC	1.27	BSC
A1	0.004	0.010	0.10	0.25	b	0.013	0.020	0.33	0.51
D	0.189	0.197	4.80	5.00	С	0.008	0.010	0.19	0.25
Н	0.228	0.244	5.80	6.20	U	0°	8°	0°	8°
Е	0.150	0.157	3.80	4.00	h	0.010	0.020	0.25	0.50
L	0.016	0.050	0.40	1.27	-	-	-	-	-

Note: Controlling dimensions are in inches. Approximate dimensions are provided in millimeters

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	information, which may change in any manner without notice.

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