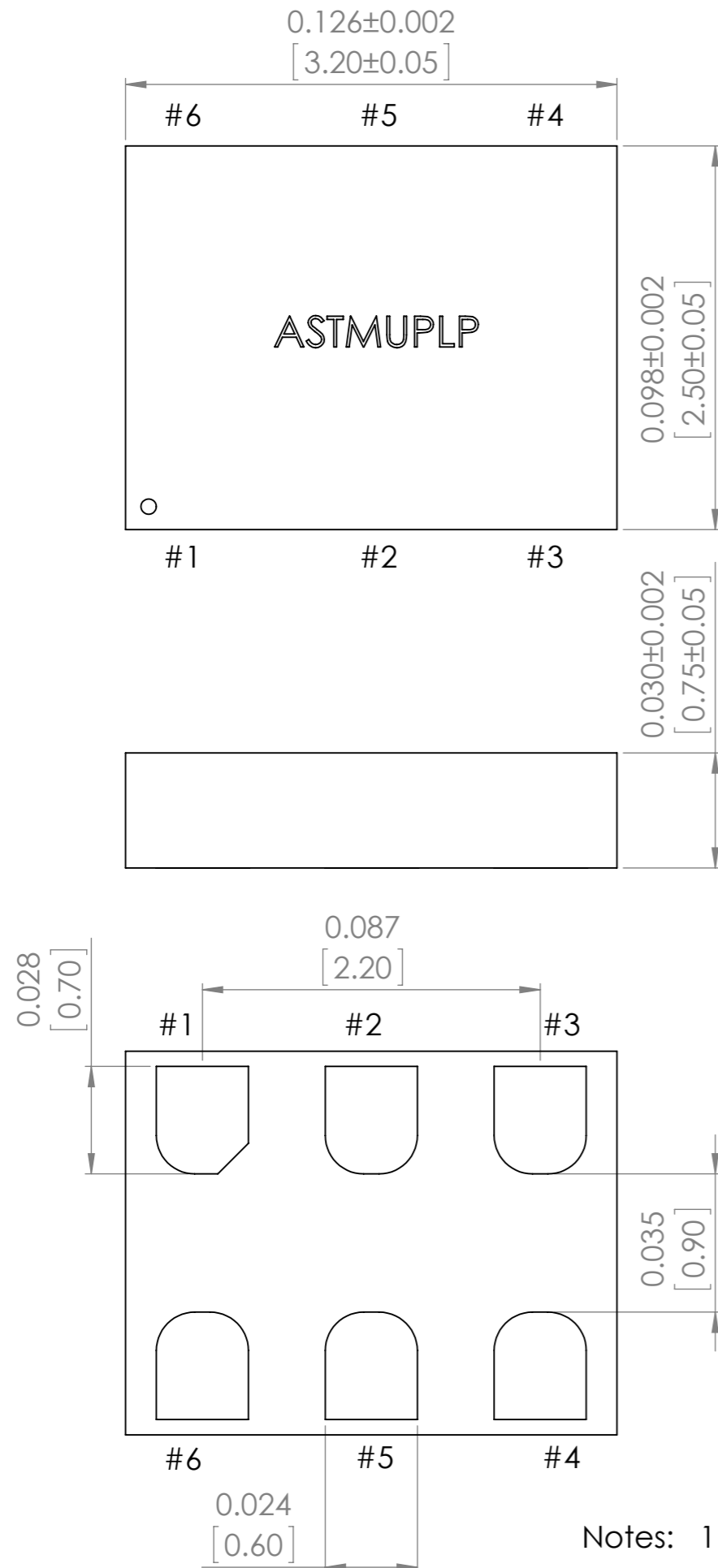
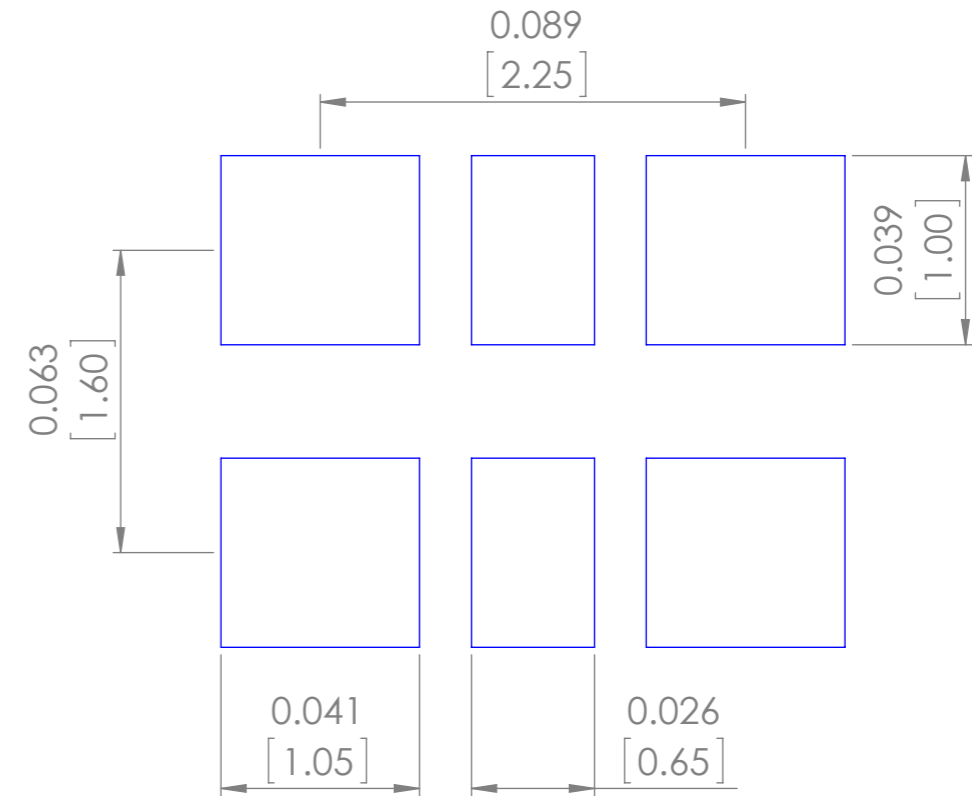


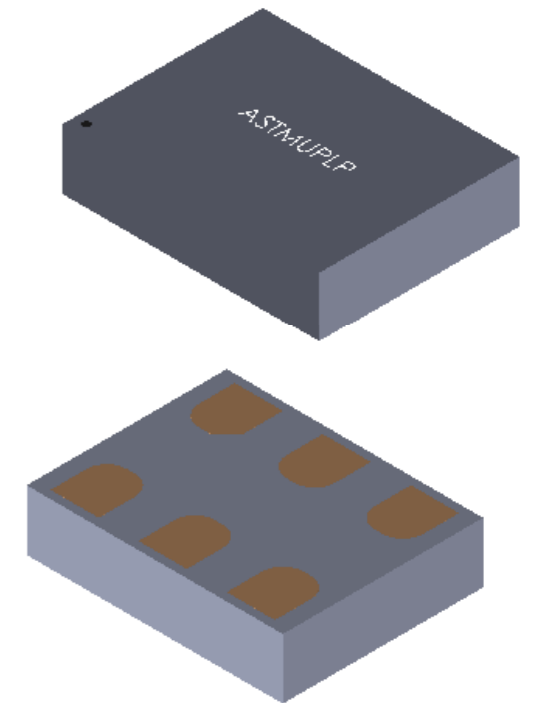
3.2 x 2.5mm Package (Option "E")



Recommended Land Pattern




Pin	Name	Functionality
1	OE/ $\overline{\text{ST}}$	Output Enable H or Open (1): specified frequency output L: output is high impedance. Only output driver is disabled
	Standby	H or Open (1): specified frequency output L: output is low (weak pull down). Device goes to sleep mode. Supply current reduces to Istd.
2	NC	No Connect Leave it floating or connect to GND for better heat dissipation
3	GND	Power Vdd power supply ground(1)
4	Out+	Output Oscillator output
5	Out-	Output Complementary oscillator output
6	Vdd	Power Power supply voltage (1)

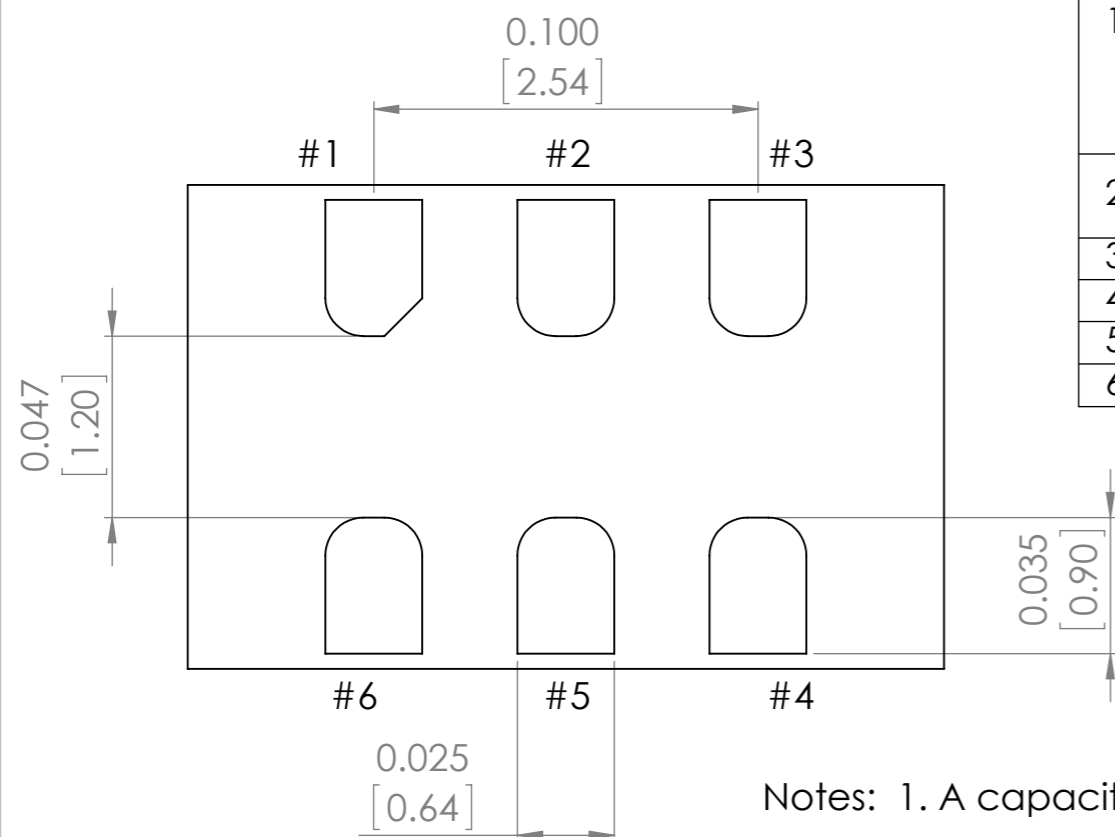
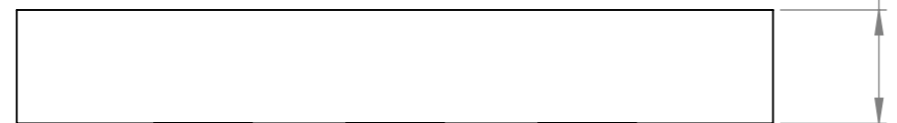
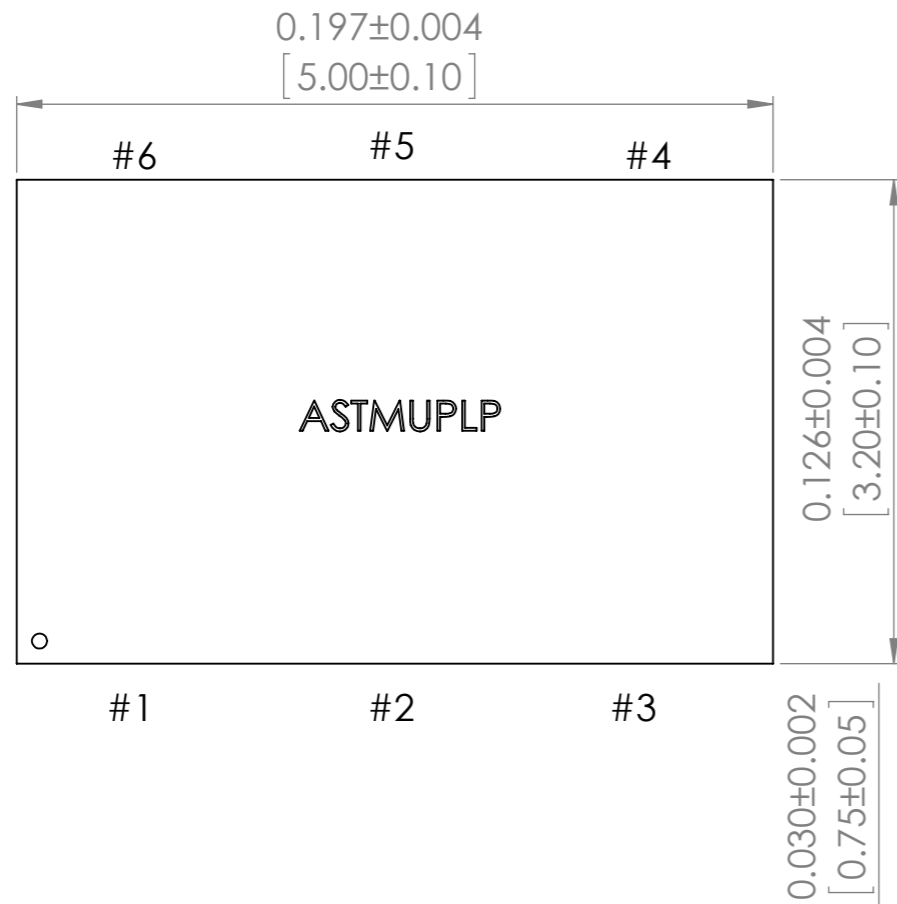


TOP PACKAGE MARKING IS FOR ILLUSTRATION PURPOSES ONLY

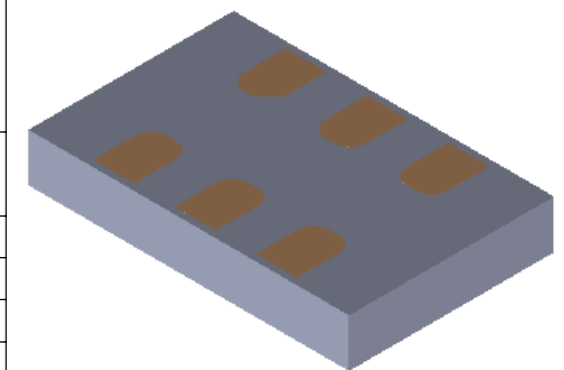
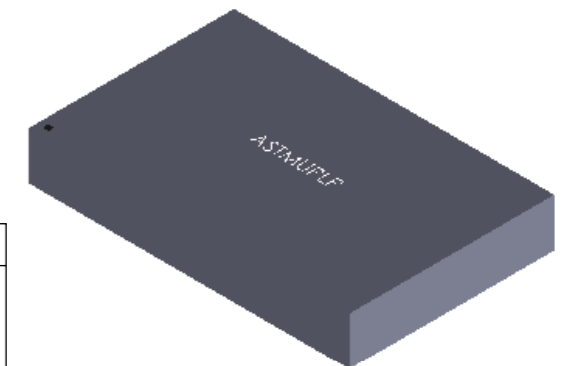
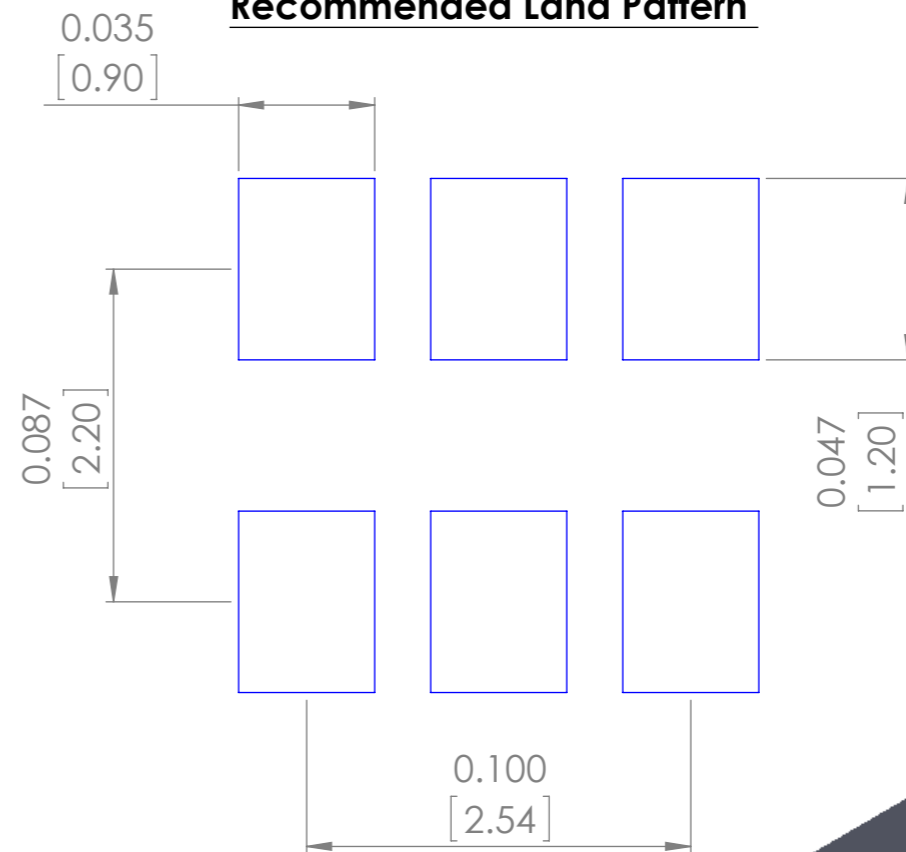
Notes: 1. A capacitor value of 0.1 μ F between Vdd and GND is recommended.

DO NOT SCALE DRAWING	REVISION	-
 Apracon LLC, 2 Faraday, Suite#B Irvine, Ca. 92618		
TITLE: High Performance, Programmable LVPECL SMD MEMS Oscillator		
DWG NO.	ASTMUPLP	A3
SCALE:25:1	SHEET 1 OF 3	

5.0 x 3.2mm Package (Option "FL")




Recommended Land Pattern



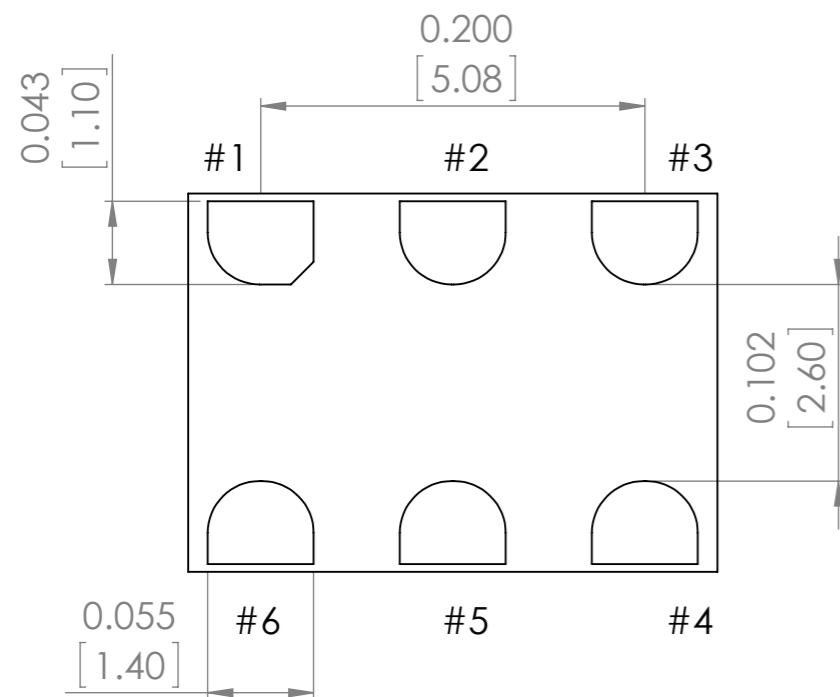
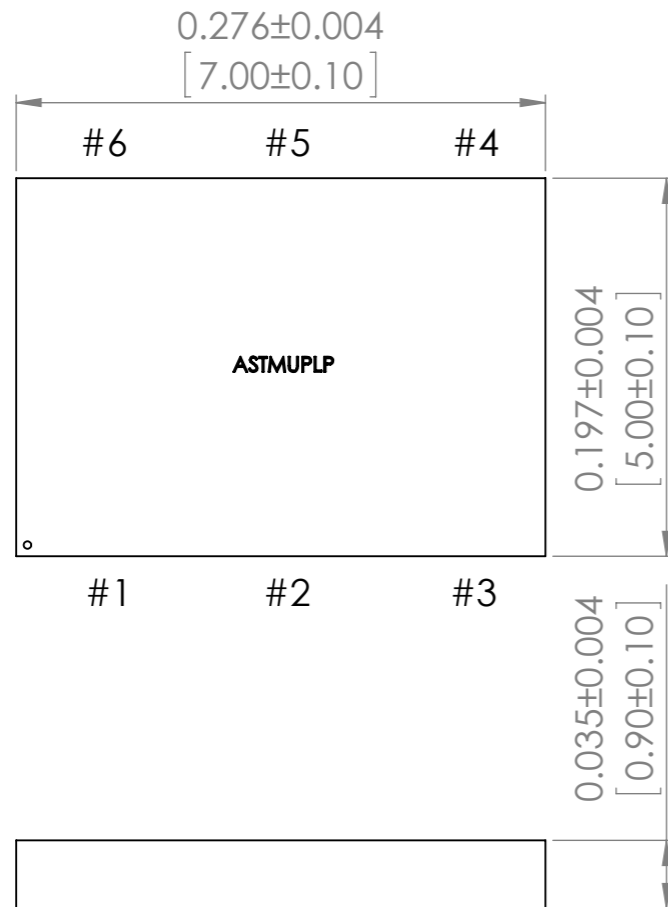
Pin	Name	Functionality
1	OE/ $\overline{\text{ST}}$	Output Enable H or Open (1): specified frequency output L: output is high impedance. Only output driver is disabled
	Standby	H or Open (1): specified frequency output L: output is low (weak pull down). Device goes to sleep mode. Supply current reduces to Istd.
2	NC	No Connect Leave it floating or connect to GND for better heat dissipation
3	GND	Power Vdd power supply ground(1)
4	Out+	Output Oscillator output
5	Out-	Output Complementary oscillator output
6	Vdd	Power Power supply voltage (1)

TOP PACKAGE MARKING IS FOR ILLUSTRATION PURPOSES ONLY

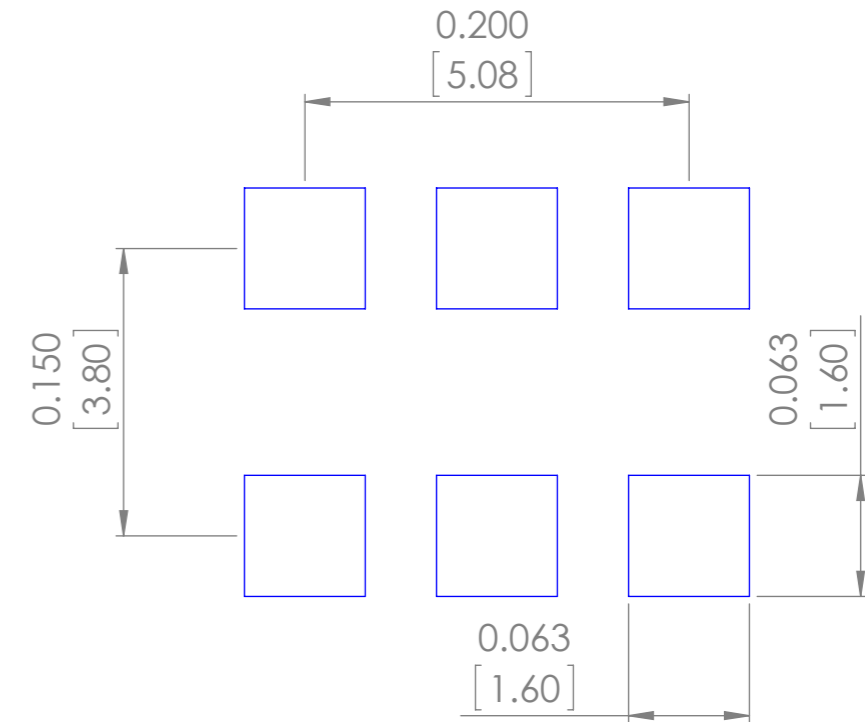
Notes: 1. A capacitor value of 0.1µF between Vdd and GND is recommended.

DO NOT SCALE DRAWING	REVISION	-
 Apracon LLC. 2 Faraday, Suite#B Irvine, Ca. 92618		
TITLE: High Performance, Programmable LVPECL SMD MEMS Oscillator		
DWG NO.	ASTMUPLP	A3
SCALE:20:1	SHEET 2 OF 3	

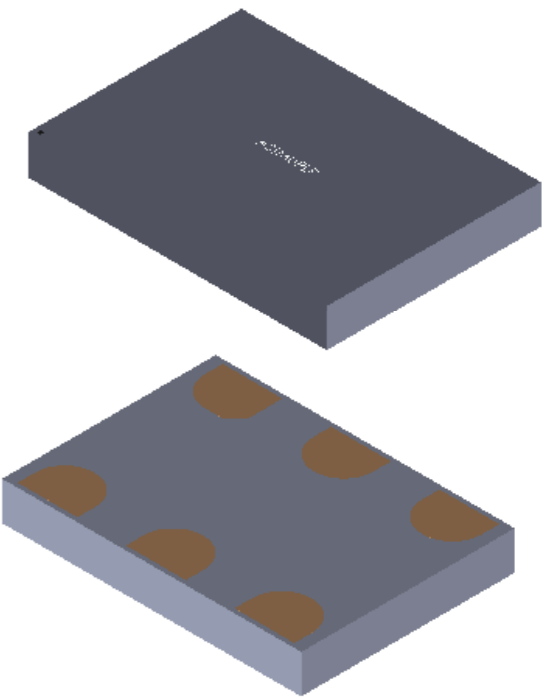
7.0 x 5.0mm Package (Option "V")



Recommended Land Pattern




Pin	Name	Functionality
1	Output Enable	H or Open (1): specified frequency output L: output is high impedance. Only output driver is disabled
	Standby	H or Open (1): specified frequency output L: output is low (weak pull down). Device goes to sleep mode. Supply current reduces to Istd.
2	NC	No Connect Leave it floating or connect to GND for better heat dissipation
3	GND	Power Vdd power supply ground(1)
4	Out+	Output Oscillator output
5	Out-	Output Complementary oscillator output
6	Vdd	Power Power supply voltage (1)



TOP PACKAGE MARKING IS FOR ILLUSTRATION PURPOSES ONLY

Notes: 1. A capacitor value of $0.1 \mu\text{F}$ between Vdd and GND is recommended.

DO NOT SCALE DRAWING	REVISION	-
 Apracon LLC. 2 Faraday, Suite#B Irvine, Ca. 92618		
TITLE: High Performance, Programmable LVPECL SMD MEMS Oscillator		
DWG NO.	ASTMUPLP	A3
SCALE:10:1	SHEET 3 OF 3	